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				Application Number	
				Filing Date	
				First Named Inventor	Stanley J. Goldman
				Group Art Unit	
Examiner Name					
Attorney Docket No.	TI-29513				
Sheet	1	of	1		

1011 U.S. PTO  
 10/036831  
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U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
C.C.	AA	6,115,586	B1	Bezzam et al.	09/05/2000	Entire Document
	AB					
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FOREIGN PATENT DOCUMENTS							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
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C.C.	CA	"A Fast Locking Scheme for PLL Frequency Synthesizers", National Semiconductor, Application Note 1000, DAVID BYRD, CRAIG DAVIS, WILLIAM O. KEESE, July, 1995, 6 pages, reprinted from the Internet at WWW.National.Com.		
C.C.	CB	"CMOS Gilbert Cell Mixer", reprinted from the Internet at: <a href="http://rcaverly.ee.vill.edu/cmossr/gilbert.htm">http://rcaverly.ee.vill.edu/cmossr/gilbert.htm</a> , October 26, 2001, 4 pages.		
C.C.	CC	"Transmitter & Receiver Architectures", Making Wireless Work, Radio Design Course, ANDREW BATEMAN, reprinted from the Internet at: <a href="http://www.avren.com/Courses/TX_RX_Architectures_plain.htm">http://www.avren.com/Courses/TX_RX_Architectures_plain.htm</a> , October 23, 2001, 21 pages.		
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Examiner Signature	C.CHOW	Date Considered	3/25/04
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